

# COPY

PATENTS

IN, THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Hirokažu MIYAZAKI

Serial No. (unknown)

Filed herewith

MOUNTING STRUCTURE OF  
SEMICONDUCTOR DEVICE AND  
MOUNTING METHOD THEREOF

**CLAIM FOR FOREIGN PRIORITY UNDER 35 U.S.C. 119**  
**AND SUBMISSION OF PRIORITY DOCUMENT**

Assistant Commissioner for Patents

Washington, D.C. 20231

Sir:

Attached hereto is a certified copy of applicant's corresponding patent application filed in Japan on February 25, 2000, under No. 49904/2000.

Applicant herewith claims the benefit of the priority filing date of the above-identified application for the above-entitled U.S. application under the provisions of 35 U.S.C. 119.

Respectfully submitted,

YOUNG & THOMPSON

By

*Benoît Castel* 37,855

Benoît Castel

Attorney for Applicant

Customer No. 000466

Registration No. 35,041

745 South 23rd Street

Arlington, VA 22202

Telephone: 703/521-2297

February 21, 2001

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日 本 国 特 許 庁

PATENT OFFICE  
JAPANESE GOVERNMENT

別紙添付の書類に記載されている事項は下記の出願書類に記載されて  
る事項と同一であることを証明する。

This is to certify that the annexed is a true copy of the following application as filed  
in this Office.

出 願 年 月 日

Date of Application:

2000年 2月25日

願 番 号

Application Number:

特願2000-049904

願 人

Applicant(s):

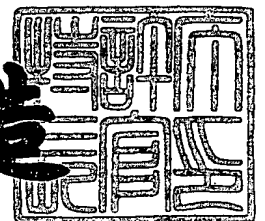
日本電気株式会社

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2000年11月17日

特許庁長官  
Commissioner,  
Patent Office

及 川 耕 造



出証番号 出証特2000-3095566